

FIG. 1.

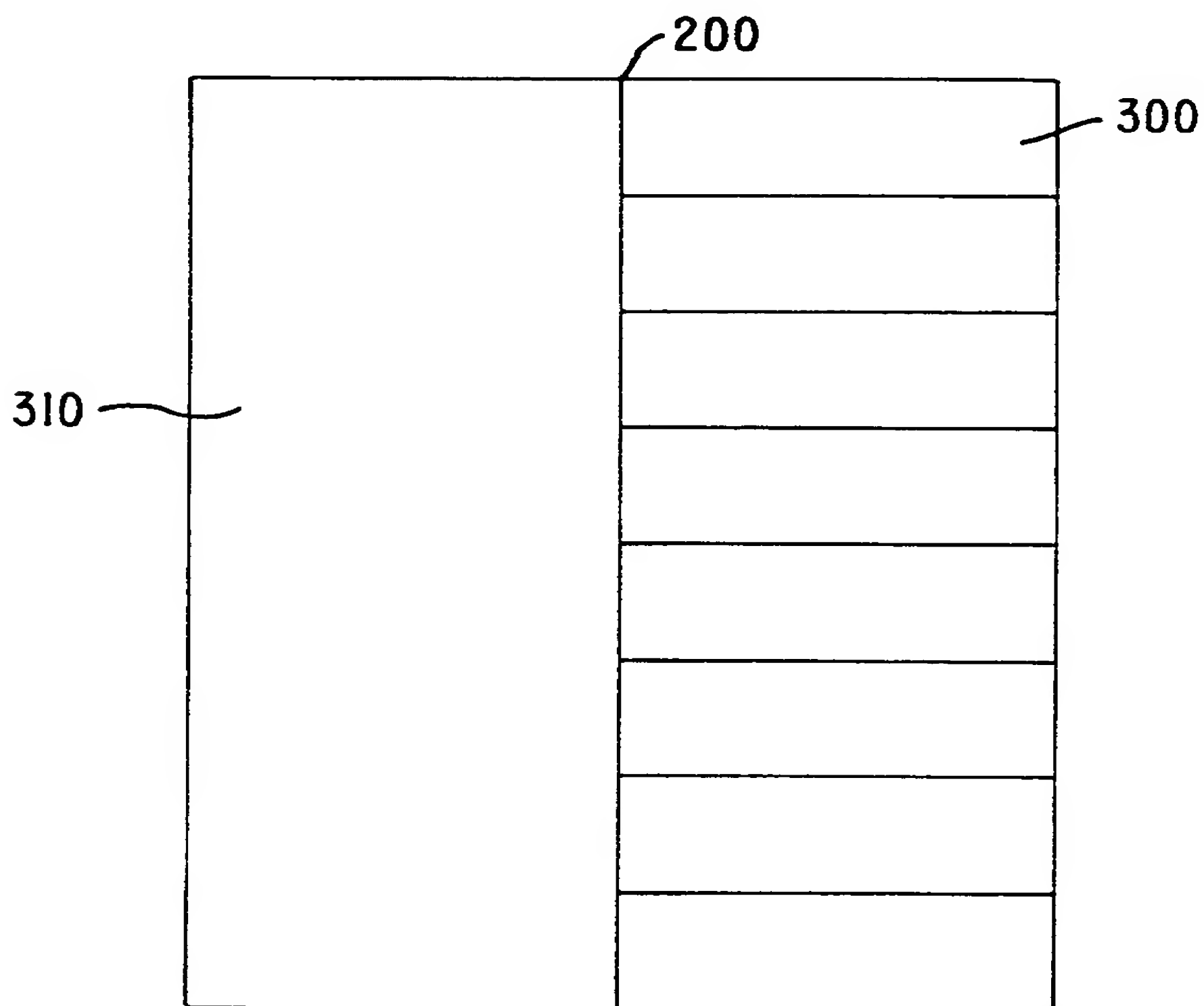


FIG. 3.

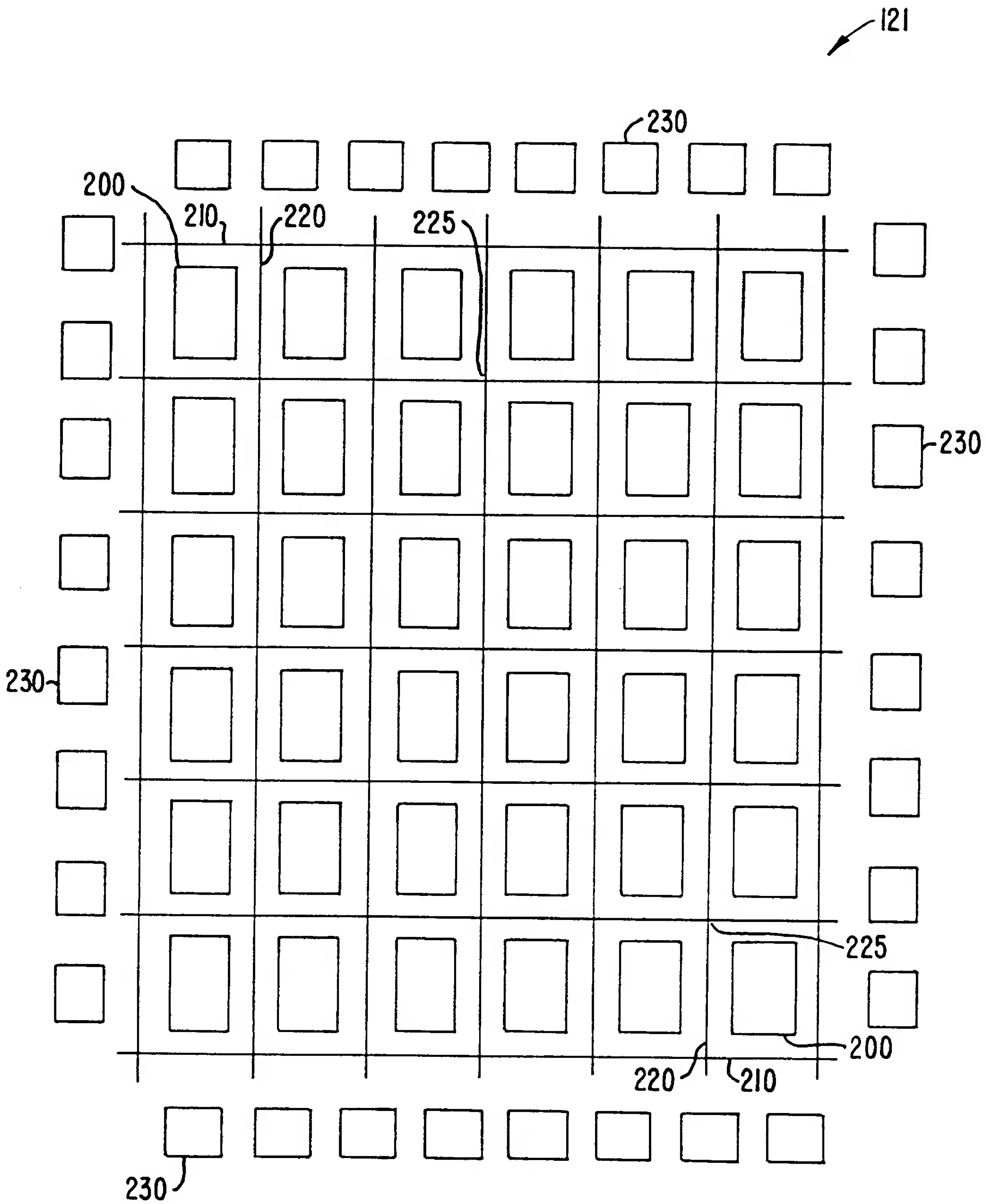


FIG. 2.

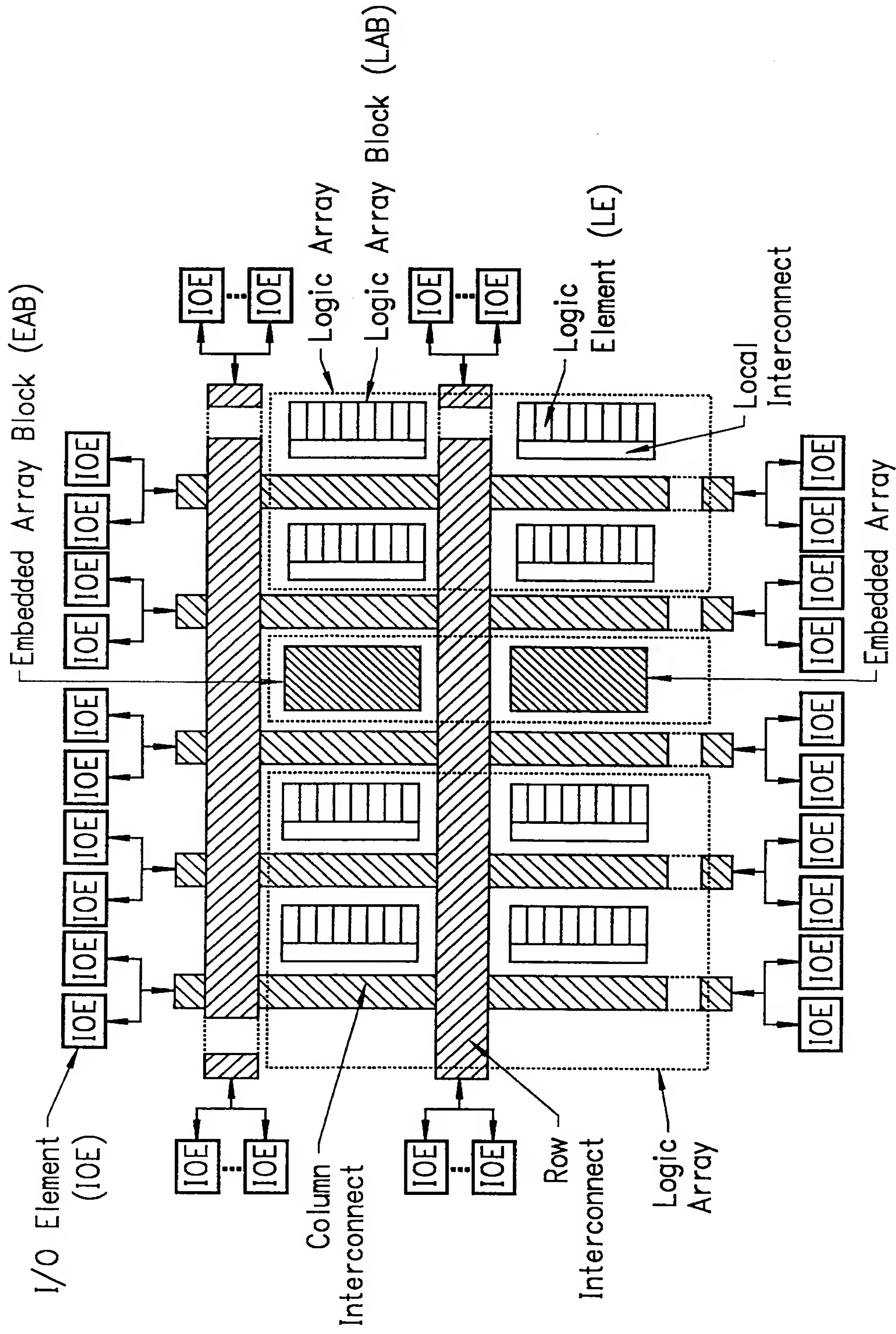
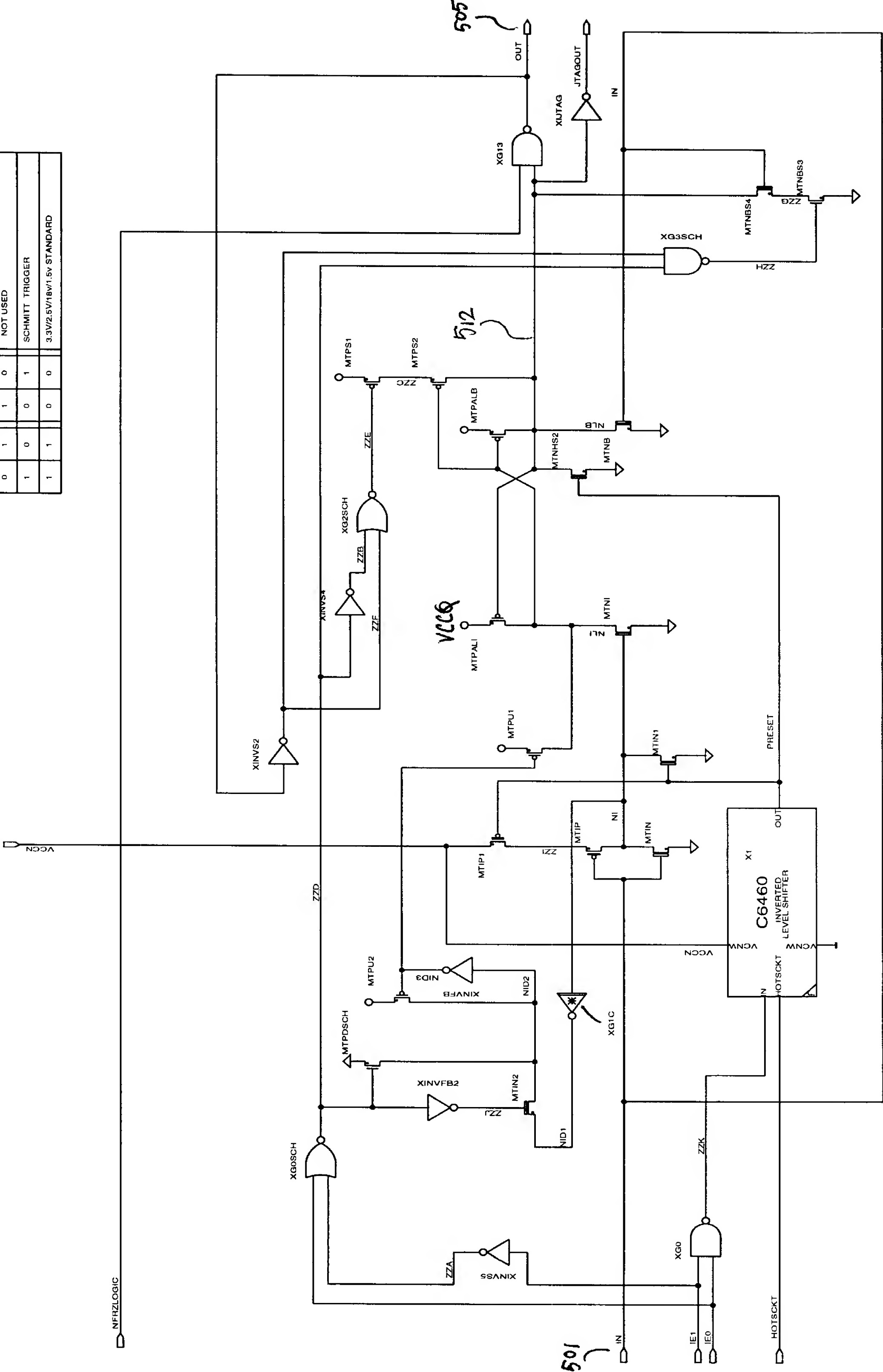


FIG. 4

NIE0	NIE1	IE0	IE1	LVTTL/CMOS STANDARD
0	0	1	1	NOT SELECTED
0	1	1	0	NOT USED
1	0	0	1	SCHMITT TRIGGER
1	1	0	0	3.3V/2.5V/1.8V/1.5V STANDARD



Layout note: Minimum VCCN/VSSN power bus=2um; min VCCQ power = 1um bus

FIGURE 5